

-30V/-10A P-Channel MOSFET

Features

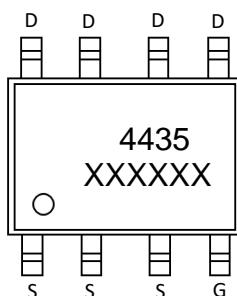
- Trench Power LV MOSFET technology
- High density cell design for Low $R_{DS(ON)}$
- High Speed switching

Product Summary

V_{DS}	$R_{DS(ON)} \text{ MAX}$	$I_D \text{ MAX}$
-30V	18mΩ@10V	-10.5A
	30mΩ@4.5V	

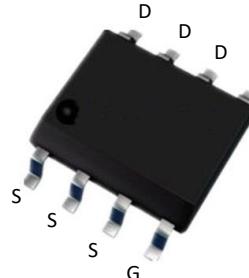
Application

- Battery protection
- Power management
- Load switch

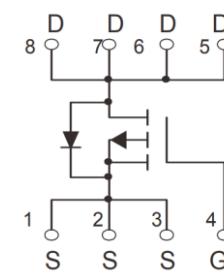


4435: Device code
XXXXXX : Code

Marking and pin assignment



SOP-8 top view



Schematic diagram

Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Symbol	Parameter	Rating	Unit
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Common Ratings (TC=25°C Unless Otherwise Noted)

V_{DS}	Drain-Source Breakdown Voltage	-30	V	
V_{GS}	Gate-Source Voltage	±20	V	
T_J	Maximum Junction Temperature	150	°C	
T_{STG}	Storage Temperature Range	-50 to 155	°C	
I_S	Diode Continuous Forward Current	Tc=25°C	-10	A

Mounted on Large Heat Sink

I_{DM}	Pulse Drain Current Tested	Tc=25°C	-50	A
I_D	Continuous Drain Current@GS=10V	Tc=25°C	-10.5	A
P_D	Maximum Power Dissipation	Tc=25°C	3	W
$R_{θJA}$	Thermal Resistance Junction-Ambient(*1 in2 Pad of 2-oz Copper), Max.)		42	°C/W

Electrical Characteristics (TJ=25°C unless otherwise noted)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Electrical Characteristics @ TJ = 25°C (unless otherwise stated)						
$BV_{(BR)DSS}$	Drain-Source Breakdown Voltage	$VGS=0V$, $ID=-250\mu A$	-30	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	$VDS=-30V$, $VGS=0V$	--	--	-1	μA
I_{GSS}	Gate-Body Leakage Current	$VGS=\pm 20V$, $VDS=0V$	--	--	± 100	nA
$V_{GS(th)}$	Gate Threshold Voltage	$VDS=VGS$, $ID=-250\mu A$	-1	-1.5	-3	V
$R_{DS(on)}$	Drain-Source On-State Resistance	$VGS=-10V$, $ID=-10A$	--	15	18	$m\Omega$
		$VGS=-4.5V$, $ID=-8A$	--	22	30	

Dynamic Electrical Characteristics @ TJ = 25°C (unless otherwise stated)

C_{ISS}	Input Capacitance	$VDS=-15V$, $VGS=0V$, $f=1MHz$	--	1500	--	pF
C_{OSS}	Output Capacitance		--	180	--	pF
C_{RSS}	Reverse Transfer Capacitance		--	150	--	pF

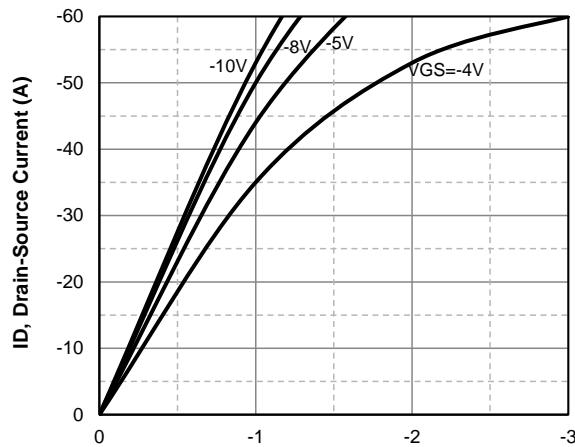
Switching Characteristics

Q_g	Total Gate Charge	$VDS=-10V$, $ID=-6A$, $VGS=-15V$	--	29	--	nC
Q_{gs}	Gate Source Charge		--	5.4	--	nC
Q_{gd}	Gate Drain Charge		--	5.4	--	nC
$t_{d(on)}$	Turn-on Delay Time		--	10	--	nS
t_r	Turn-on Rise Time	$VDD=-15V$, $ID=-6A$, $VGS=-10V$, $RG=2.5\Omega$	--	45	--	nS
$t_{d(off)}$	Turn-Off Delay Time		--	55	--	nS
t_f	Turn-Off Fall Time		--	60	--	nS

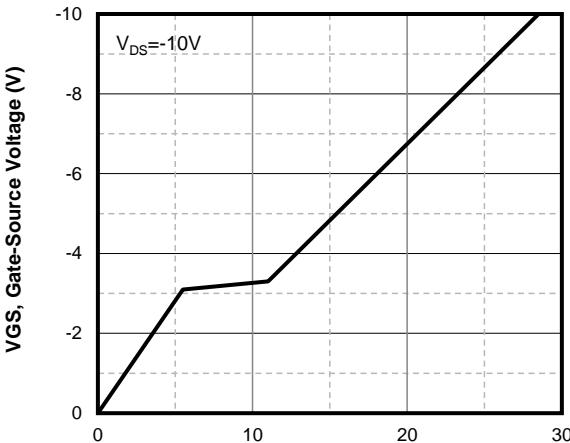
Source- Drain Diode Characteristics

V_{SD}	Forward on voltage	$Tj=25^\circ C$, $Is=-10A$,	--	-0.8	-1.2	V
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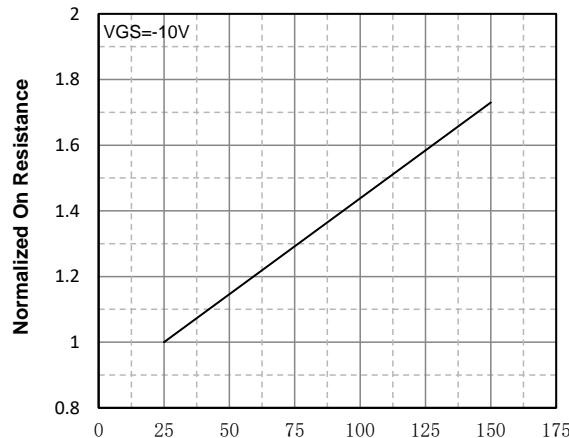
Typical Operating Characteristics



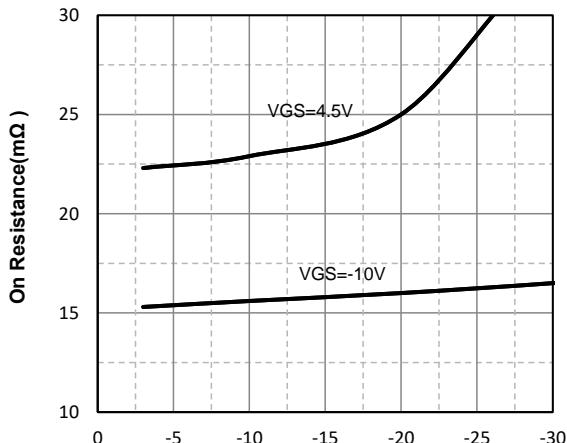
V_{DS}, Drain -Source Voltage (V)
Fig1. Typical Output Characteristics



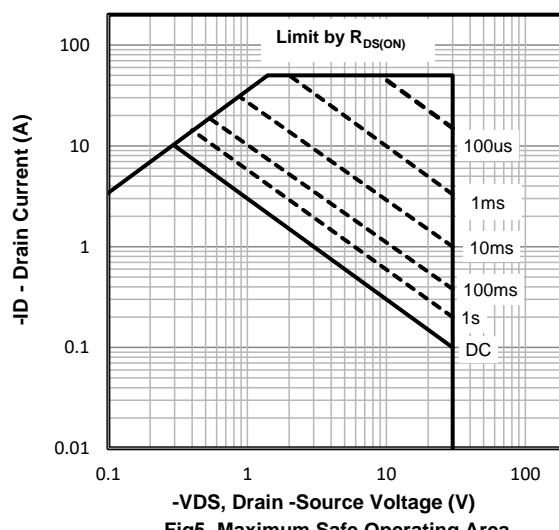
Q_g -Total Gate Charge (nC)
Fig2. Typical Gate Charge Vs.Gate-Source Voltage



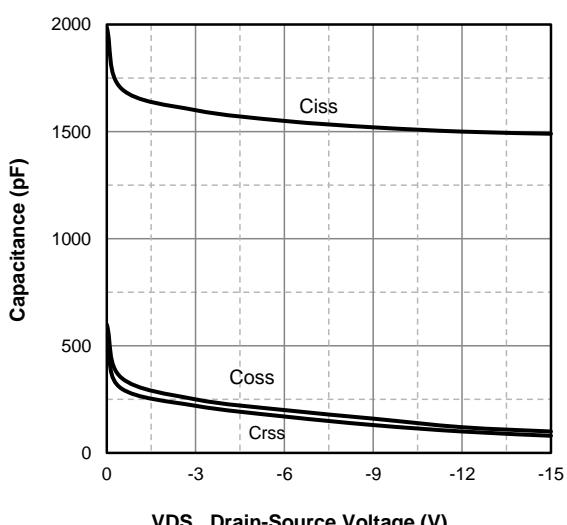
T_j - Junction Temperature (°C)
Fig3. Normalized On-Resistance Vs. Temperature



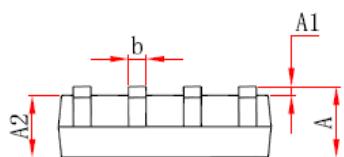
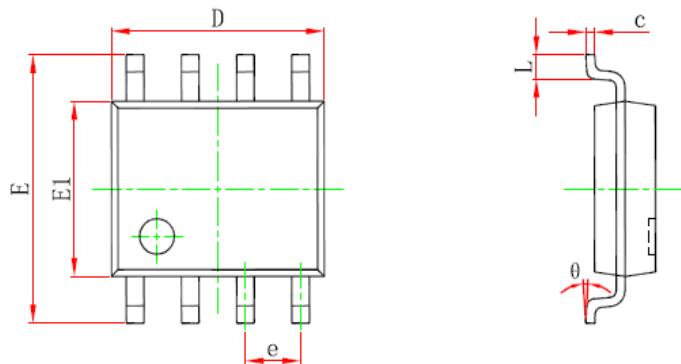
I_D, Drain-Source Current (A)
Fig4. On-Resistance Vs. Drain-Source Current



-I_D - Drain Current (A)
Fig5. Maximum Safe Operating Area



C_{iss}
C_{oss}
C_{rss}
V_{DS} , Drain-Source Voltage (V)
Fig6 Typical Capacitance Vs.Drain-Source Voltage

SOP-8 Package information

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.450	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.700	5.100	0.185	0.201
e	1.270 (BSC)		0.050 (BSC)	
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°